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From-MOTOROLA

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**MOTOROLA**

**FAX TRANSMITTAL SHEET**

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**4** Number of Pages (including this page)

Date: November 18, 2003  
To: **OFFICE OF OFFICIAL PATENT EXAMINATION'S  
FILING RECEIPT CORRECTIONS**  
Location: United States Patent and Trademark Office  
Fax No.: (703) 746-9195  
From: Kenneth A. Haas 42,614

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**MESSAGE:**

Enclosed herewith, please find a Request to Correct Filing Receipt for filing in the below-identified application.

GROUP ART UNIT:	2818
SERIAL NO.:	10/646,231
FILED:	8/22/2003
INVENTOR:	Brazis et al
DOCKET NO.:	CML01198T

PATENT  
CML01198T

## UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT(S): Brazis et al  
SERIAL NO.: 10/646,231  
FILE DATE: 8/22/2003  
GROUP ART UNIT: 2818  
TITLE: METHOD AND APPARATUS FOR READING AND WRITING TO SOLID-STATE MEMORY

CERTIFICATE OF MAILING

I HEREBY CERTIFY THAT THIS CORRESPONDENCE IS BEING DEPOSITED WITH THE UNITED STATES POSTAL SERVICE AS FIRST CLASS MAIL IN AN ENVELOPE ADDRESSED TO: Office of Initial Patent Examination, Customer Service Center, COMMISSIONER FOR PATENTS, ALEXANDRIA, VA 22313, ON: 11/18/2003

Date of Deposit

MOTOROLA INC.  
Name of Assignee  
SIGNATURE

11/18/2003

DATE

CORRECTION OF FILING RECEIPT

Office of Initial Patent Examination  
Customer Service Center  
Commissioner for Patents  
Alexandria, VA 22313


Sir:

It is respectfully pointed out that the Filing Receipt for the above-identified patent application is incorrect. The title of the application is incorrect. "METHOD AND APPARATUS FOR READING AND WRITING TO SOLID-STATE MEMORY" should be METHOD AND APPARATUS FOR READING AND WRITING TO SOLID-STATE MEMORY".

Please correct the filing receipt to include the corrected title as follows METHOD AND APPARATUS FOR READING AND WRITING TO SOLID-STATE MEMORY". A copy of Page One of the Specification is attached along with a copy of the Filing Receipt with the change noted thereon.

No fee is due since the error was not that of Applicant.

Respectfully submitted,

  
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## UNITED STATES PATENT AND TRADEMARK OFFICE

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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/846,231	08/22/2003	2818	750	CML01198T	5	15	3

22917  
 MOTOROLA, INC.  
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NOV 18 2003

PATENT DEPT  
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CONFIRMATION NO. 1387

FILING RECEIPT

1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52 53 54 55 56 57 58 59 60 61 62 63 64 65 66 67 68 69 70 71 72 73 74 75 76 77 78 79 80 81 82 83 84 85 86 87 88 89 90 91 92 93 94 95 96 97 98 99 100  
 \*OC000000011271500\*

Date Mailed: 11/14/2003

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

## Applicant(s)

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N n-Publication Request: No

Early Publication Request: No

## Title

Method and apparatus for reading an writing to solid-state memory

AND

## METHOD AND APPARATUS FOR READING AND WRITING TO SOLID-STATE MEMORY

### Field of the Invention

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The present invention relates generally to solid-state memory and in particular, to a method and apparatus for reading and writing to solid-state memory.

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### Background of the Invention

Large-scale (>1GB) solid-state memory storage is a rapidly expanding market, particularly for multimedia applications. Currently these storage devices have not been successfully applied in usage scenarios where large storage capabilities are needed. For example, personal computers still utilize hard-disk storage as a primary storage mechanism. In order for manufacturers to utilize solid-state memory devices in place of hard-disk storage and for high-reliability applications, the performance of such solid-state memory devices must be improved. One way to improve performance of solid-state memory storage is to increase the performance of read-write operations so that such operations occur more efficiently, and data are well protected from device failures.

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### Brief Description of the Drawings

FIG. 1 is a block diagram of solid-state storage means.

FIG. 2 is a flow chart showing operation of the solid-state storage means of FIG. 1.

FIG. 3 is a flow chart showing a method for detection of non-operational die and the actions of a controller in such a situation.

FIG. 4 is a flow chart for dynamically updating a performance model database.

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